

IN THE SPECIFICATION:

The specification as amended below with replacement paragraphs shows added text with underlining and deleted text with ~~strikethrough~~.

Please REPLACE the paragraph beginning at page 6, line 32, with the following paragraph:

The daughter board 71-1 includes: a daughter-board main body 72-1; the daughter-board male connector 102-1 that is provided at the lower end of the daughter-board main body 72-1; a mounting operation lever 73-1 that is provided at the upper end of the daughter-board main body 72-1; a card edge connector 74-1 that is mounted on a half area 72-1a at the Z2~~Y2~~ side of the daughter-board main body 72-1 and connects the memory mounting boards 81-1 to the daughter-board main body 72-1; terminal resistances 75-1 that prevent signal reflections and are mounted on either surface of the other half area 72-1b at the Z1 side of the daughter-board main body 72-1; and bypass capacitors 76-1 that bypass a dielectric layer in the daughter-board main body 72-1, which has a multi-layer structure. Reference numerals 77-1 and 77-2 indicate electronic parts mounting regions to which the terminal resistances 75-1 and the bypass capacitors 76-1 are mounted.

Please REPLACE the paragraph beginning at page 7, line 20, with the following paragraph:

Each of memory mounting boards 81-1 has the end 82-1a insert-connected to the card edge connector 74-1. The memory mounting boards 81-1 protrude horizontally from the half area 72-1a at the Z2~~Y2~~ side of the vertically standing daughter board 71-1.

Please REPLACE the paragraph beginning at page 7, line 26, with the following paragraph:

The second board unit 70-2 includes a vertically standing daughter board 71-2 and four memory mounting boards 81-2 that horizontally protrude in a comb-like fashion from the Z1~~Y1~~ side half of the daughter board 71-2, as shown in FIGS. 6 and 7. The second board unit 70-2 is

substantially the same as the first board unit 70-1, except that the memory mounting boards 81-2 are located on the Z1-side half of the daughter board 71-2.

Please REPLACE the paragraph beginning at page 7, line 35, with the following paragraph:

The daughter board 71-2 includes: a daughter-board main body 72-2; the daughter-board male connector 102-2 that is provided at the lower end of the daughter-board main body 72-2; a lever 73-2 that is provided at the upper end of the daughter-board main body 72-2; a card edge connector 74-2 that is mounted to a half area 72-2b at the Z1 side of the daughter-board main body 72-2 and connects the memory mounting boards 81-2 to the daughter-board main body 72-2; terminal resistances 75-2 that are mounted to the inner surface of a half area 72-2a at the Z2 side of the daughter-board main body 72-2 and both of the surfaces near the end on the Z1 side; and bypass capacitors 76-2. Reference numerals 78-1, 78-2, and 78-3 and ~~78-2~~, indicate electronic parts mounting regions to which the terminal resistances 75-2 and the bypass capacitors 76-2 are mounted.

Please REPLACE the paragraph beginning at pages 8, line 22, with the following paragraph:

Each of the memory mounting boards 81-2 has the end 82-2a insert-connected to the card edge connector 74-2. The memory mounting boards 81-2 horizontally protrude from the half area 72-2b at the Z1~~Z1~~ side of the vertically standing daughter board 71-2.